

DETAILED ACTION

1. This office action is responsive to communication filed on March 9, 2010.

Continued Examination Under 37 CFR 1.114

2. A request for continued examination under 37 CFR 1.114, including the fee set forth in 37 CFR 1.17(e), was filed in this application after final rejection. Since this application is eligible for continued examination under 37 CFR 1.114, and the fee set forth in 37 CFR 1.17(e) has been timely paid, the finality of the previous Office action has been withdrawn pursuant to 37 CFR 1.114. Applicant's submission filed on March 9, 2010 has been entered.

Information Disclosure Statement

3. The Information Disclosure Statement (I.D.S.) filed May 5, 2010 was received and has been considered by the Examiner.

EXAMINER'S AMENDMENT

4. An examiner's amendment to the record appears below. Should the changes and/or additions be unacceptable to applicant, an amendment may be filed as provided by 37 CFR 1.312. To ensure consideration of such an amendment, it MUST be submitted no later than the payment of the issue fee.
5. Authorization for this examiner's amendment was given in a telephone interview with Babak Akhlaghi (No. L0250) on May 6, 2010.

The claims are to be amended as follows:

Claims 4, 12, 14, 16, 17, 20, 22, 25-27 and 31-34 are cancelled.

Claim 36, Page 10

Line 2 of claim 36, delete "the first signal transmission method provides a first operation outputting selection signals to all the pixels included in the single line pixel group"

Allowable Subject Matter

6. Claims 5, 15, 18, 19, 21, 23, 28-30 and 35-38 are allowed.
7. The following is an examiner's statement of reasons for allowance:

Consider claim 5, the prior art of record does not teach nor reasonably suggest an operation switching circuit for outputting the selection signals from the shift register to each pixel, the operation switching circuit configured to switch between a first signal transmission method in which the selection signals are sequentially output to all pixels either in the vertical direction or the horizontal direction and a second signal transmission method in which the selection signals are continuously output to pixels having color filters of the same color either in the vertical direction or the horizontal direction, and wherein **in both of the first and second signal transmission methods, each of the selection signals of the shift register is output via the operation switching circuit to a corresponding pixel included in the same pixel group arranged in the same direction as the shift register, such that all pixels in the pixel group receive a selection signal from the shift register in the first signal**

transmission method and all pixels in the pixel group receive a selection signal from the shift register in the second signal transmission method, as recited in claim 5. Particularly, the prior art of record does not teach or suggest sequentially outputting selection signals to all pixels in a vertical or horizontal direction in a first method and continuously outputting selection signals to pixels having color filters of the same color in the vertical or horizontal direction in a second method while still outputting the selection signals to all of the pixels of the same pixel group as during the first method.

Claims 15, 18, 19, 21, 23 and 28-30 are allowed as depending from an allowed claim 5.

Consider claim 35, the prior art of record does not teach nor reasonably suggest an operation switching circuit for switching between two signal transmission methods when outputting the selection signals from the shift register to the single line pixel group, wherein in the first signal transmission method of the two signal transmission methods, the operation switching circuit outputs the selection signals without changing an order of the selection signals, and in the second signal transmission method of the two signal transmission methods, the operation switching circuit outputs the selection signals, changing the order of the selection signals partially, and **in both of the first and second signal transmission methods, each of the selection signals of the shift register is output via the operation switching circuit to a corresponding pixel**

included in the same single line pixel group, such that all pixels in the single line pixel group receive a selection signal from the shift register in the first signal transmission method and all pixels in the single line pixel group receive a selection signal from the shift register in the second signal transmission method.

Particularly, the prior art of record does not teach or suggest outputting selection signals to a single line pixel group without changing the order of the selection signals in a first method, and outputting selection signals while changing the order of the selection signals partially in a second method while still outputting the selection signals to all of the pixels of the same pixel group as during the first method.

Claims 36-38 are allowed as depending from an allowed claim 35.

8. Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to ALBERT H. CUTLER whose telephone number is (571)270-1460. The examiner can normally be reached on Mon-Thu (9:00-5:00).

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Sinh Tran can be reached on (571) 272-7564. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

/Sinh Tran/
Supervisory Patent Examiner, Art
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